

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	5129607
<b>Application Number:</b>	10595157
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	3496
<b>Title of Invention:</b>	Method of Manufacturing Multi-Layer Circuit Board
<b>First Named Inventor/Applicant Name:</b>	Toshiaki Takenaka
<b>Customer Number:</b>	52349
<b>Filer:</b>	Andrew Lawson Dunlap/sarah pedersen
<b>Filer Authorized By:</b>	Andrew Lawson Dunlap
<b>Attorney Docket Number:</b>	2006-0223A
<b>Receipt Date:</b>	09-APR-2009
<b>Filing Date:</b>	09-MAR-2006
<b>Time Stamp:</b>	16:57:12
<b>Application Type:</b>	U.S. National Stage under 35 USC 371

### Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$180
RAM confirmation Number	3090
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part /.zip	Pages (if appl.)
1		AttachA_P04030802_IDS.pdf	148069 8a8ddbebbf249a823893c4fb4bb1ceca803 5b909	yes	4
<b>Multipart Description/PDF files in .zip description</b>					
<b>Document Description</b>		<b>Start</b>		<b>End</b>	
		Transmittal Letter		1	3
		Information Disclosure Statement (IDS) Filed (SB/08)		4	4

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### Information:

2	Foreign Reference	oa.PDF	182458 d64f9713c72c25481925c3a04e3153528a5f 0a92	no	2
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### Information:

3	Foreign Reference	145.PDF	364212 db8c748cdf5eb3754c828fb91697cd916e4 b2994	no	8
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4	Foreign Reference	87.PDF	450987 128f20dbdb6241b77065b159c86d3757cb 1e16cf	no	8
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### Information:

5	Fee Worksheet (PTO-06)	fee-info.pdf	30088 8061283603a533a2839c0f3aeabbce9e3bc bc89b	no	2
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